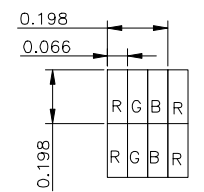
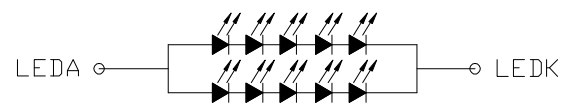


PIN ASSIGNMENT	
1	VDD
2	GND
3	SPI_SCLK/I2C_SCL
4	MISO/I2C_SDA
5	MOSI/I2C_SAO
6	CS/I2C_SA1
7	INT
8	PD
9	MODE
10	AUDIO_OUT
11	NC
12	NC
13	NC
14	NC
15	NC
16	NC
17	BLVDD
18	BLVDD
19	BLGND
20	BLGND



DETAIL A  
(SCALE 35:1)



INTERNAL BACKLIGHT CTRCUIT DIAGRAM

- NOTES FOR TFT:
1. DISPLAY TYPE: TFT/TRANSMISSIVE/NORMALLY WHITE.
  2. VIEWING DIRECTION: 12 O'CLOCK.
  3. OPERATING VOLTAGE: VDD=3.3V(TYP.).
  4. CONTROLLER: FT801.
  5. OPERATING TEMP.: -20°C ~ 70°C.
  6. STORAGE TEMP.: -30°C ~ 80°C.
  7. BACKLIGHT: 10-CHIP WHITE LED,BUILT-IN BACKLIGHT DRIVER.
  8. SURFACE LUMINANCE: 460CD/M<sup>2</sup>(TYP.).
  9. SURFACE TREATMENT: ANTI-GLARE.
  10. GENERAL TOLERANCE: ±0.2mm.
  11. ROHS COMPLIANT.

- NOTES FOR CTP:
1. 4.3 INCH PROJECTIVE CAPACITIVE TOUCH PANEL.
  2. OPERATION VOLTAGE: 3.3V(TYP.)
  3. COVER GLASS + SENSOR GLASS + FPCCA.
  4. RESOLUTION: 480 X 272 DOTS
  5. DRIVER IC: FT5446DQS (SMT).
  6. MULTI FINGER: UP TO 5.
  7. HARDNESS OF COVER SURFACE: 7H.

MULTI-INNO TECHNOLOGY CO.,LTD.			
DRAWN BY:	JACK	2018.05.09	SCALE : 1/1
CHECKED BY:			UNIT: mm
APPROVED BY:			DESCRIPTION: TFT & CTP MODULE
VERSION NO:	01		

VER.	FIRST ISSUE	JACK	2018.05.09
	REVISED DESCRIPTION	REVISER	DATE